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	Subclass

ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. 	PATENT DATE 
SCANNED  Q.A. 	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/966741	F	438	580+	2814	J. AVACH

APPENDIX

Kazuichiro Itonaga

TIME

Semiconductor device and method for manufacturing the same

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PTO-204
12/03

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				ISSUE FEE	
	(Primary Examiner)	(Date)		Amount Due	Date Paid
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	(Legal Instruments Examiner)	(Date)			

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